



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **TESHIROGI, Kazuo et al.**

Serial No.: **10/708,067 (Div. of S. N. 10/101,169)**

Filed: **February 6, 2004**

***P0353-0698-020334A**

For: **SEMICONDUCTOR SUBSTRATE JIG AND METHOD
FOR MANUFACTURING A SEMICONDUCTOR DEVICE**

INFORMATION DISCLOSURE STATEMENT
PURSUANT TO 37 CFR 1.97(b)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

February 17, 2004

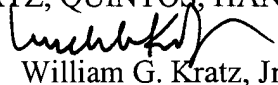
Sir:

This Information Disclosure Statement is being filed in order to comply with Applicant's duty of disclosure under 37 CFR 1.56. The document listed on the Form PTO-1449 was made of record in parent application Serial No. 10/101,169. Accordingly, a copy of the reference is not attached.

The Commissioner is authorized to charge our Deposit Account No. 01-2340 for any fee which is deemed by the Patent and Trademark Office to be required to effect consideration of this statement.

Respectfully submitted,

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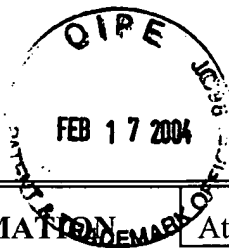
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PATENT TRADEMARK OFFICE

Enclosure: PTO-1449



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	Applicant(s): TESHIROGI, Kazuo et al.	
	Filing Date: February 6, 2004	Group Art Unit:

U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
_____	AA					
_____	AB					
_____	AC					
_____	AD					
_____	AE					

FOREIGN PATENT DOCUMENTS

Document No.	Date	Country	Translation (Yes or No)
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_____ AH			
_____ AI			
_____ AJ			

OTHER DOCUMENTS

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Examiner	Date Considered
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